

Notice of References Cited	Application/Control No. 09/943,078	Applicant(s)/Patent Under Reexamination ABBOTT, TODD R.	
	Examiner Erik Kielin	Art Unit 2813	Page 1 of 1

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	B	US-5,599,728	02-1997	Hu et al.	438/289
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	U	Wolf, Silicon Processing for the VLSI Era, Vol. 2-Process Integration, Lattice Press: Sunset Beach CA, 1990, pp. 20-22, 39-41.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.